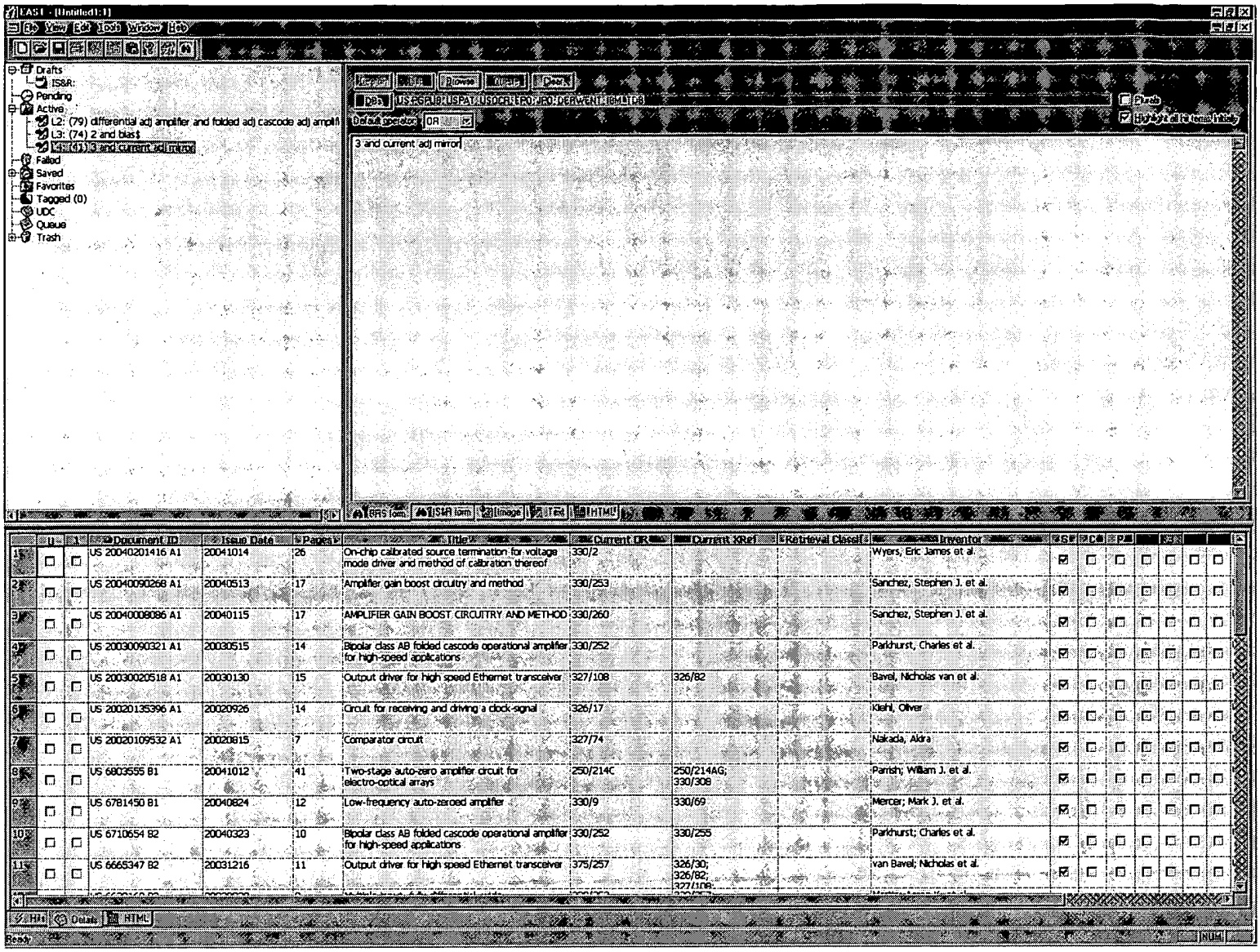

			Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Classif	Inventor	S	C	P				
1	☐	☐	US 20040201416 A1	20041014	26	On-chip calibrated source termination for voltage mode driver and method of calibration thereof	330/2			Wyers, Eric James et al.	☑	☐	☐	☐	☐	☐	☐
2	☐	☐	US 20040090268 A1	20040513	17	Amplifier gain boost circuitry and method	330/253			Sanchez, Stephen J. et al.	☑	☐	☐	☐	☐	☐	☐
3	☐	☐	US 20040008086 A1	20040115	17	AMPLIFIER GAIN BOOST CIRCUITRY AND METHOD	330/260			Sanchez, Stephen J. et al.	☑	☐	☐	☐	☐	☐	☐
4	☐	☐	US 20030090321 A1	20030515	14	Bipolar class AB folded cascode operational amplifier for high-speed applications	330/252			Parkhurst, Charles et al.	☑	☐	☐	☐	☐	☐	☐
5	☐	☐	US 20030020518 A1	20030130	15	Output driver for high speed Ethernet transceiver	327/108	326/82		Bavel, Nicholas van et al.	☑	☐	☐	☐	☐	☐	☐
6	☐	☐	US 20020135396 A1	20020926	14	Circuit for receiving and driving a clock signal	326/17			Klehl, Oliver	☑	☐	☐	☐	☐	☐	☐
7	☐	☐	US 20020109532 A1	20020815	7	Comparator circuit	327/74			Nakada, Akira	☑	☐	☐	☐	☐	☐	☐
8	☐	☐	US 6803555 B1	20041012	41	Two-stage auto-zero amplifier circuit for electro-optical arrays	250/214C	250/214AG; 330/308		Parrish; William J. et al.	☑	☐	☐	☐	☐	☐	☐
9	☐	☐	US 6781450 B1	20040824	12	Low-frequency auto-zeroed amplifier	330/9	330/69		Mercer; Mark J. et al.	☑	☐	☐	☐	☐	☐	☐
10	☐	☐	US 6710654 B2	20040323	10	Bipolar class AB folded cascode operational amplifier for high-speed applications	330/252	330/255		Parkhurst; Charles et al.	☑	☐	☐	☐	☐	☐	☐
11	☐	☐	US 6665347 B2	20031216	11	Output driver for high speed Ethernet transceiver	375/257	326/30; 326/82; 327/108		van Bavel; Nicholas et al.	☑	☐	☐	☐	☐	☐	☐